

**Electronics Engineering Department**  
**Sardar Vallabhbhai National Institute of Technology, Surat, Gujarat, India**

**International Conference on**  
**"Advances in VLSI and Embedded Systems (AVES-2019)"**  
**December 20-21, 2019**

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**Pre-conference tutorials on December 19, 2019 (FREE of CHARGE)**

**Tentative Schedule**

Time	December 19, 2019	December 20, 2019	December 21, 2019	
08:30 am			Registration & Breakfast	
09:00 am		Registration & Breakfast (2 <sup>nd</sup> Floor ECED New Bldg)	(2 <sup>nd</sup> Floor ECED New Bldg)	
09:30 am	Pre-Conference Tutorial-I AGK (EC406N)	Inaugural Ceremony (EC406N)	Keynote II	
10:00 am			RMC (EC406N)	
10:30 am		***Tea Break***	***Tea Break***	
11:00 am	***Tea-Break***	Plenary Talk-I GS (EC406N)	Keynote III	
11:30 am	Pre-Conference Tutorial-I AGK (EC406N)		RP (EC406N)	
12:00 noon			<b>Paper Presentation Sessions</b>	
12:30 pm			Session II-A (EC201N)	Session II-B (EC202N)
01:00 pm	***Lunch Break***	***Lunch Break***	***Lunch Break***	
02:00 pm	Pre-Conference Tutorial-II VS (EC406N)	Keynote I	Keynote IV	
02:30 pm		VS (EC406N)	SJ (EC406N)	
03:00 pm				
03:30 pm	***Tea-Break***	***Tea Break***	***Tea Break***	
04:00 pm	Pre-Conference Tutorial-II VS (EC406N)	<b>Paper Presentation Sessions</b>		
04:30 pm		Session I-A (EC201N)	Session I-B (EC202N)	Session III-A (EC201N)
05:00 pm		Session I-C (EC222N)		Session III-B (EC202N)
05:30 pm	***Program Close***	***Program Close***	*** Valedictory & Conference Close***	