

Electronics Engineering Department

International Conference on Advances in VLSI and Embedded Systems

December 20-21, 2019

ABOUT SVNIT

- Established in 1961
- Premier Institute of National Importance since 2007
- NIRF ranking in top 100

ABOUT DEPARTMENT

- Established in 1982
- One UG & Two PG programmes
- Well Equipped Labs
- Learned faculty
- Around 100 research scholars

ABOUT SURAT

- Fast progressing Smart City in Gujarat, India.
- Known for diamonds, textile, culture and food.
- City located at Tapi river, near Arabian sea.

GENERAL CHAIRS

A. D. Darji, P. J. Engineer, Zuber Patel, Rasika Dhavse
SVNIT, Surat

HIGHLIGHTS

- Finest keynote speeches by world renowned speakers
- Plenary talks from industry and academia
- Radical topics in VLSI and Embedded Systems Exhibition
- Opportunity for networking
- Possible future collaborations

REVIEW & PUBLICATION

- Double blind peer reviewed by at-least two reviewers.
- Selected and presented/exhibited manuscripts will be sent for possible publication in Springer LNEE.

IMPORTANT DATES

- Manuscript Submission Deadline: ~~Sept. 15, 2019~~ ~~Sept. 30, 2019~~
Oct. 20, 2019
- Notification: Nov. 15, 2019
- Registration Starts: Nov. 16, 2019
- Camera-ready Paper Submission & Registration Closes: Nov. 30, 2019

REGISTRATION FEES	INDIAN NATIONALS (INR)	FOREIGN DELEGATES (USD)
STUDENTS	3500	200
ACADEMIA	6000	250
INDUSTRY	10000	300
ATTENDEES	2500	150

VISIT US AT

www.aves2019.org

CONTACT US AT

aves2019.svnit@gmail.com

PAPER TRACKS

VLSI Design

System-level design, Advances in digital design, Low power design, Analog and mixed signal design, FPGA design and reconfigurable systems, RF/Wireless systems, System-on-chip designs, Networks-on-chip Design

Devices and Emerging Technology

Semiconductor device simulation, Device modeling, Fabrication and characterization, Device-circuit interactions, Sensors and transducers, MEMS / NEMS, Organic Electronics, Spintronics, 2-D material for electronic devices, Device physics design and circuits using non- silicon materials, Fabrication and packaging technologies

Testing and Verification

Design for testability, Test generation and fault simulation, Built-in self-test, Verification (simulation and formal), Design for manufacturability and yield analysis, Test-memories and regular logic arrays

Embedded Systems

Embedded systems for industrial automation and power electronics, Embedded systems for defence, healthcare, automotive and signal processing, Technologies for secure embedded circuits and systems, IoT and wearable devices, High performance embedded systems

CAD for VLSI

Logic and behavioural synthesis, Placement, routing and floor planning; CAD tools, Design automation



AVES-2019: Call For Papers



Springer

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